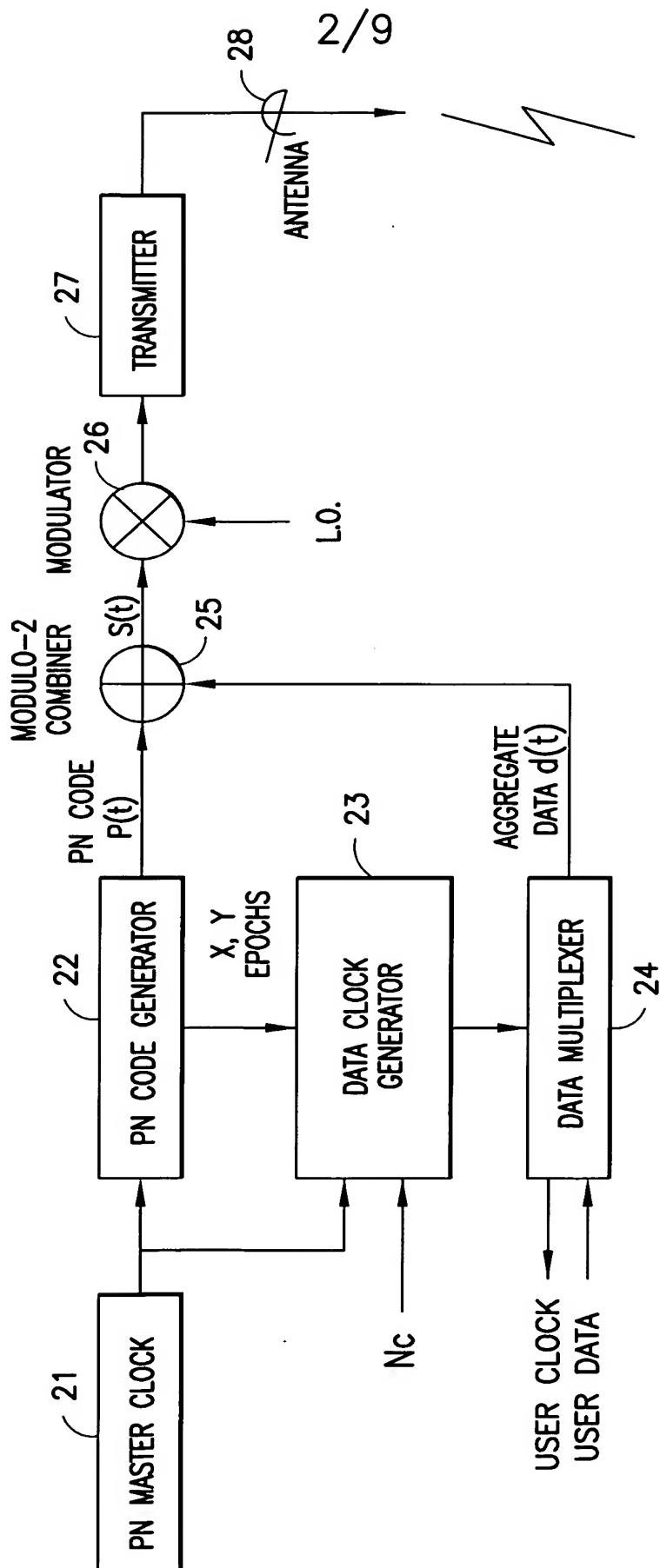


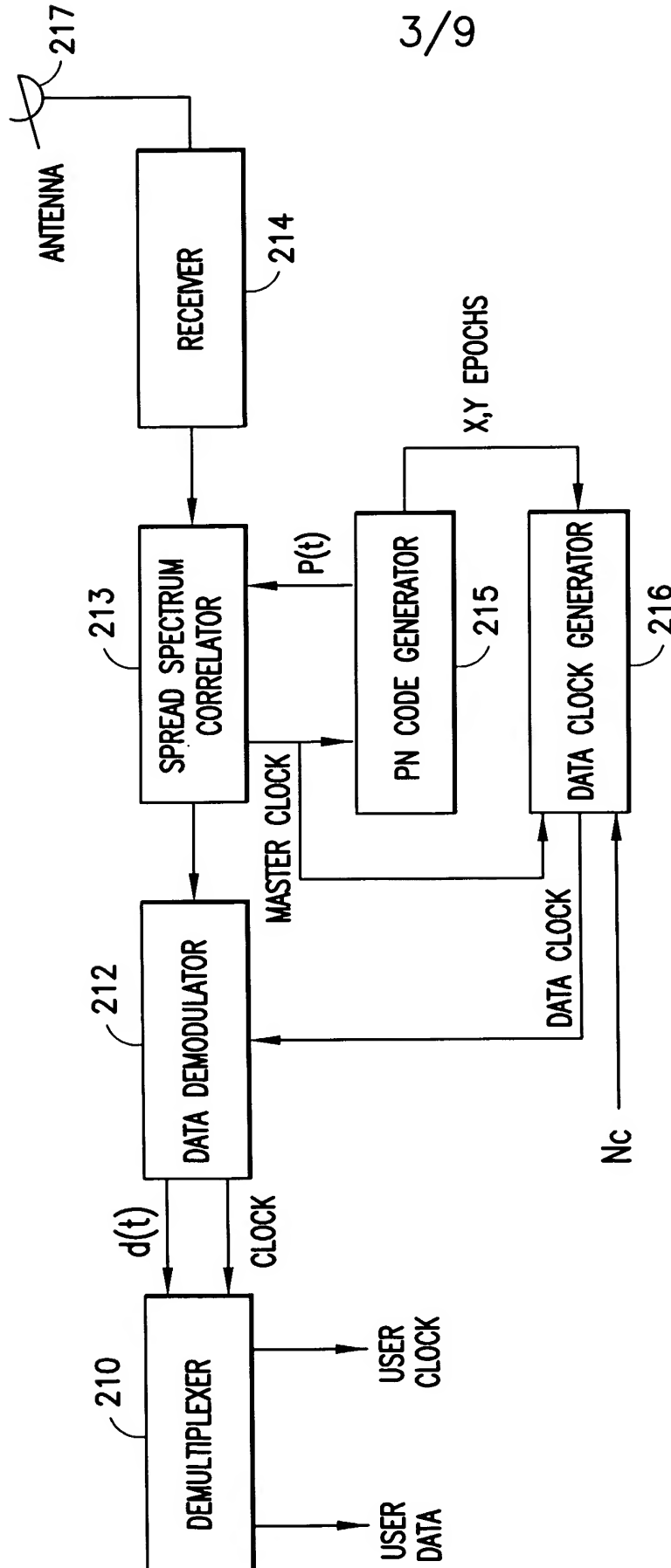
FIG.1



DSSS TRANSMITTER
FIG. 2A



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DSSS RECEIVER
FIG. 2B



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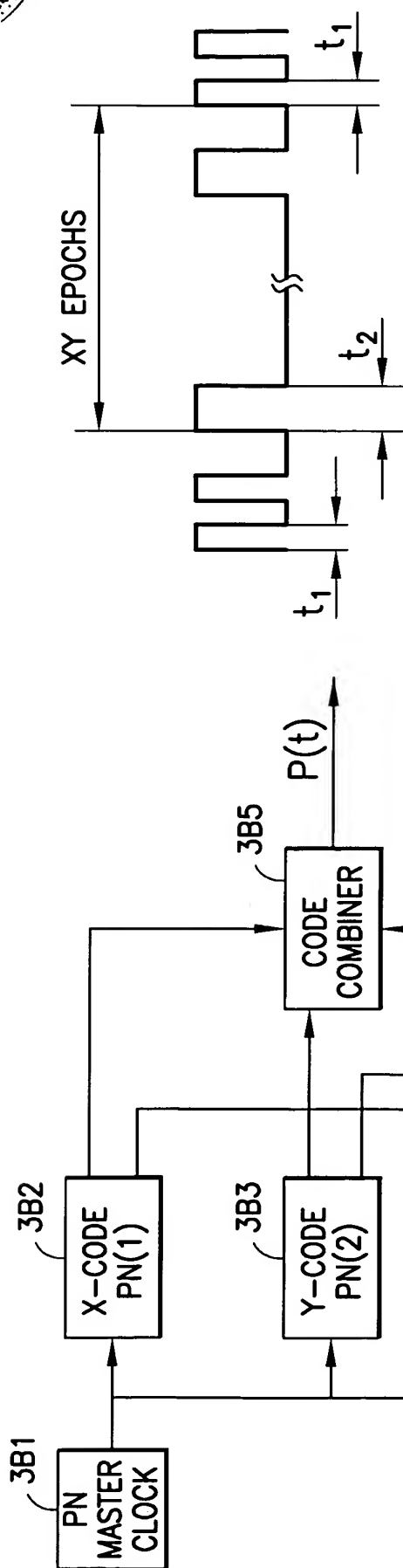
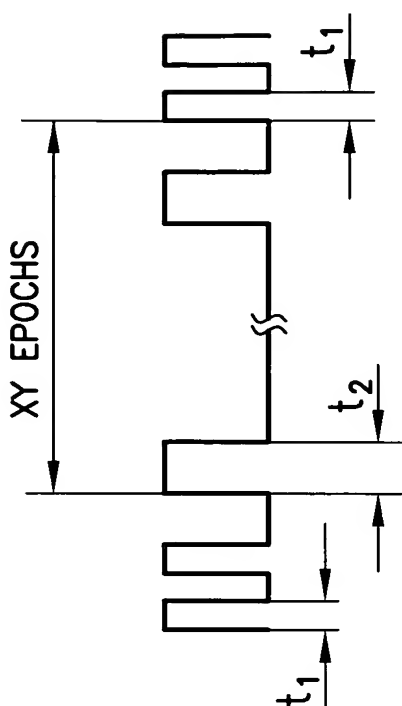


FIG. 3A

FIG. 3C



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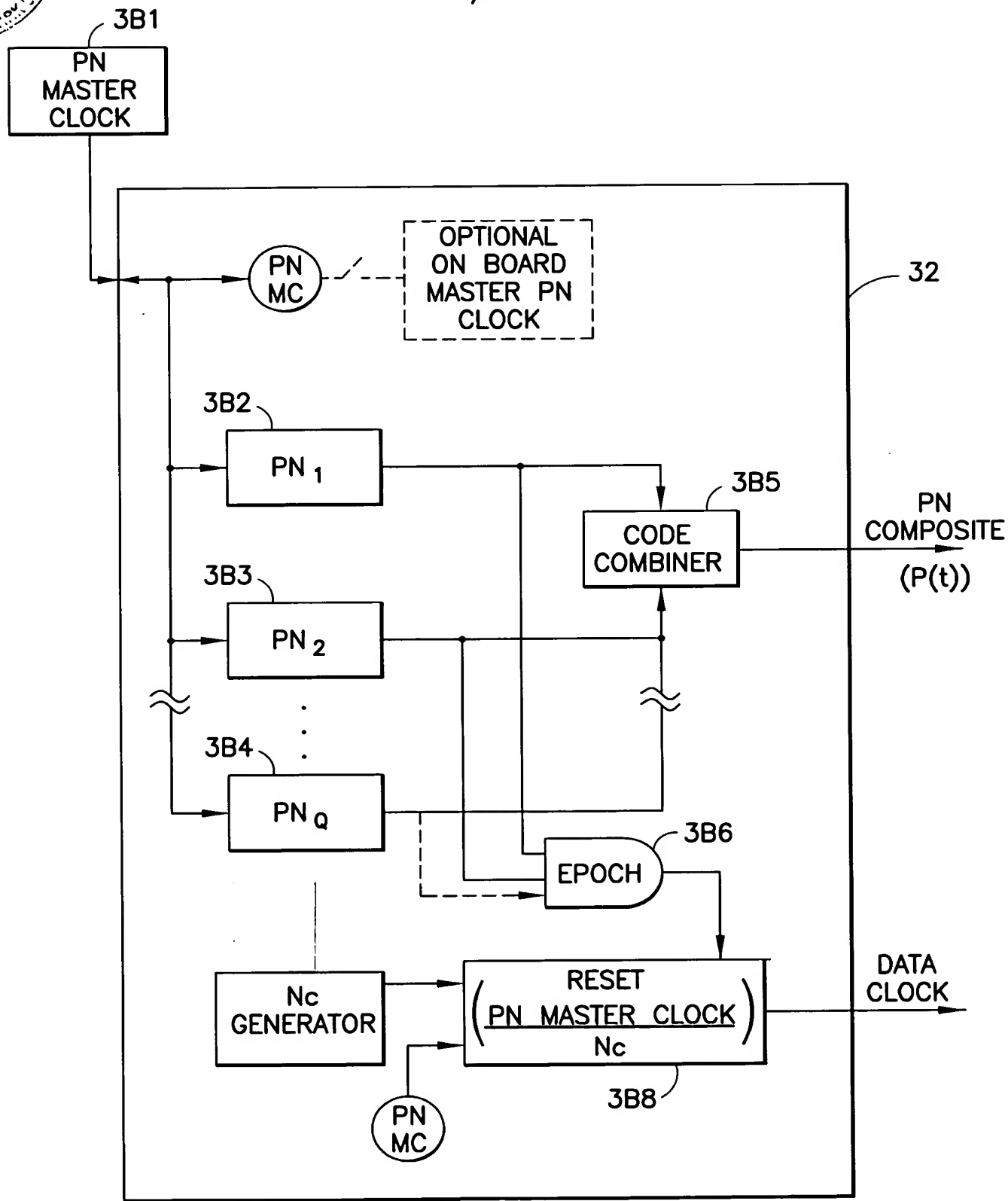


FIG.3B



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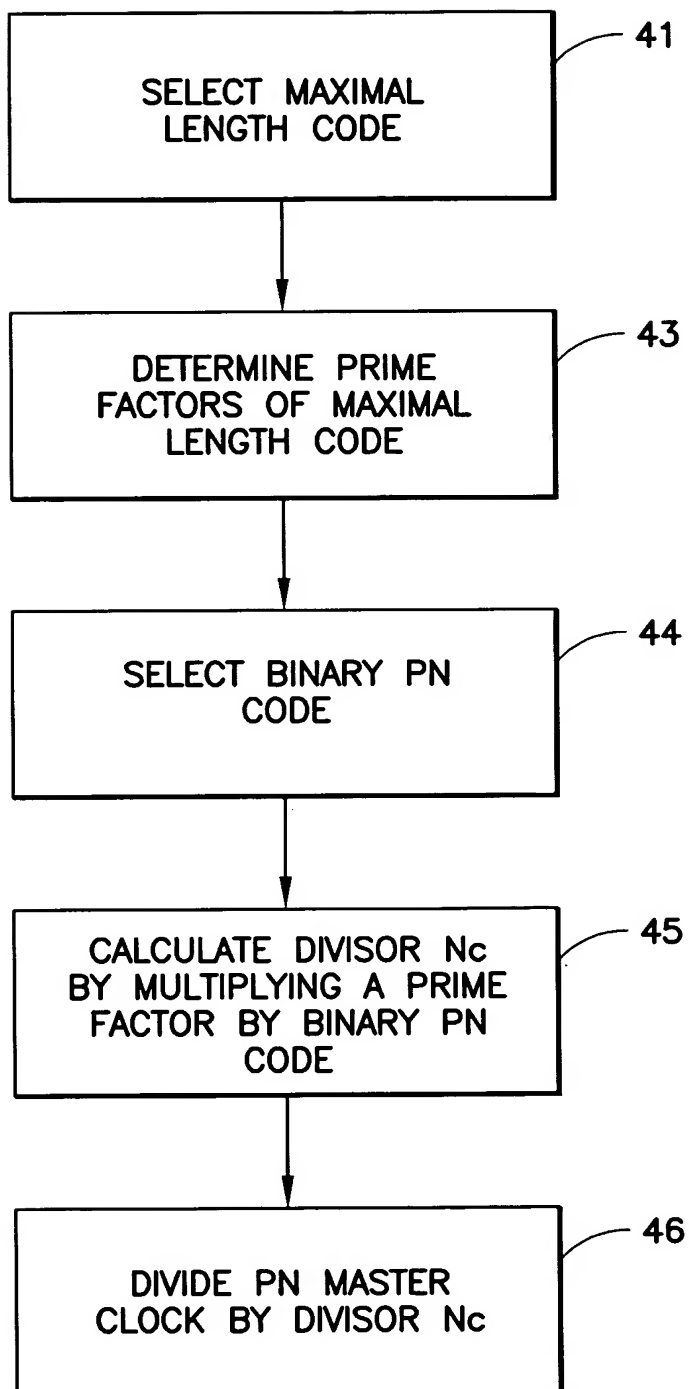


FIG.4



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DIVISOR N_c FORMULATION TABLE

n	2^n	2^{n*3}	2^{n*3^2}	2^{n*5}	2^{n*7}	2^{n*13}
0	1	3	9	5	7	13
1	2	6	18	10	14	26
2	4	12	36	20	28	52
3	8	24	72	40	56	104
4	16	48	144	80	112	208
5	32	96	288	160	224	416
6	64	192	576	320	448	832
7	128	384	1152	640	896	1664
8	256	768	2304	1280	1792	3328
9	512	1536	4608	2560	3584	6656
10	1024	3072	9216	5120	7168	13312
11	2048	6144	18432	10240	14336	26624
12	4096	12288	36864	20480	28672	53248

FIG.5

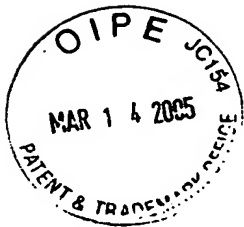


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SELECTED DIMSOR Nc AND STEP SIZE

Nc	STEP	Nc	STEP	Nc	STEP	Nc	STEP
1	1	24	4	160	32	1152	128
2	1	28	4	192	32	1280	256
3	1	32	4	224	32	1536	256
4	1	36	4	256	32	1792	256
5	1	40	4	288	32	2048	256
6	1	48	8	320	64		
7	1	56	8	384	64		
8	1	64	8	448	64		
9	1	72	8	512	64		
10	2	80	8	576	64		
12	2	96	16	640	128		
14	2	112	16	768	128		
16	2	128	16	896	128		
18	2	144	16	1024	128		
20	2						

FIG.6



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DIVISION RATION VS DIVISION STEP

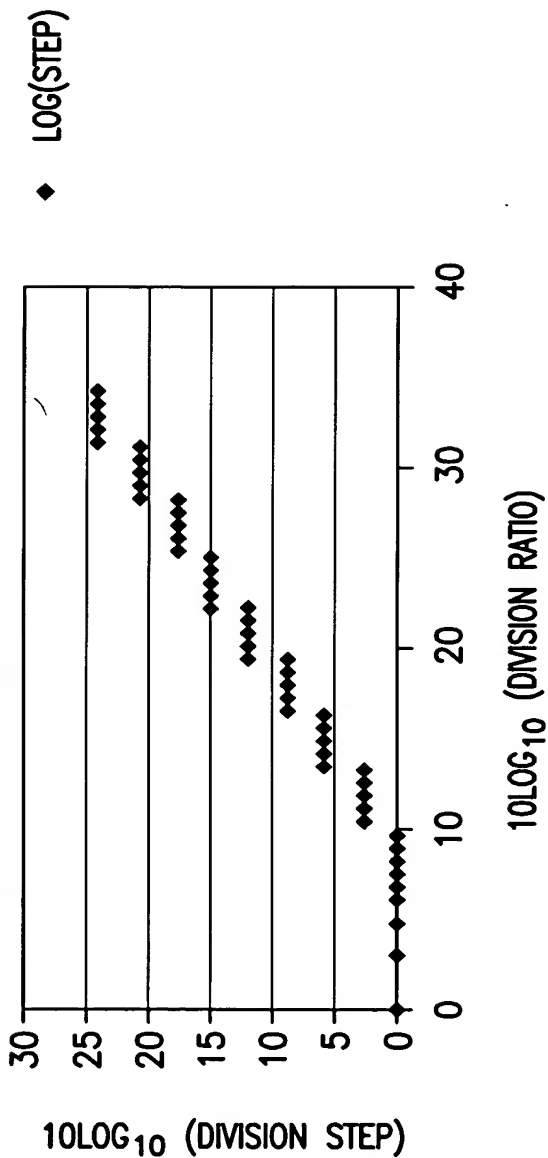


FIG.7